

CC-Link IE Field Slave IP

Compatible with Mitsubishi Electric CP220

CC-Link IE Field

Features

- Supports CC-Link IE Field and Intelligent device station
- Supports Cyclic transmission and Transient transmission
 - RX/RY = 2048 bits each, RW_r/RW_w = 1024 words each
- Light CPU load

Merits of the IP embedded in FPGA

- Integrate user logics in addition to CP220 equivalent functions
- Implements various industrial protocols other than CC-Link IE Field on the single FPGA
- Provides a platform for multi low-volume applications
- Simple software migration from existing CP220

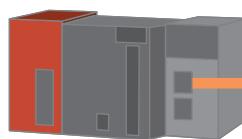
CC-Link IE Field IP Core Topology Check Demo

CC IE Field Diagnostics

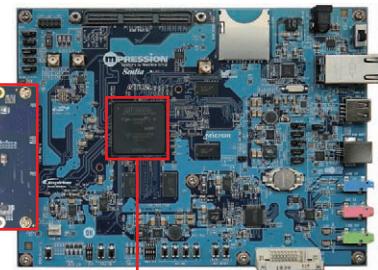


“Remote I/O”

Mitsubishi Electric:
(L61P+ L26CPU+ LJ71GF11-T2)

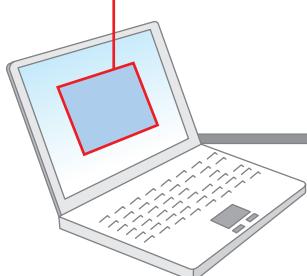


Macnica:
Sodia - Cyclone V ST SoC Board

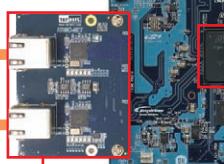


“Master”

Mitsubishi Electric:
(Q00UJCPU_V+ QJ71GF11-T2_V)

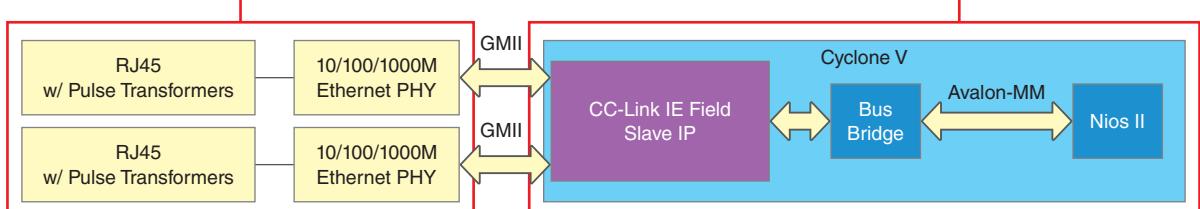


Terasic:
HSMC-NET

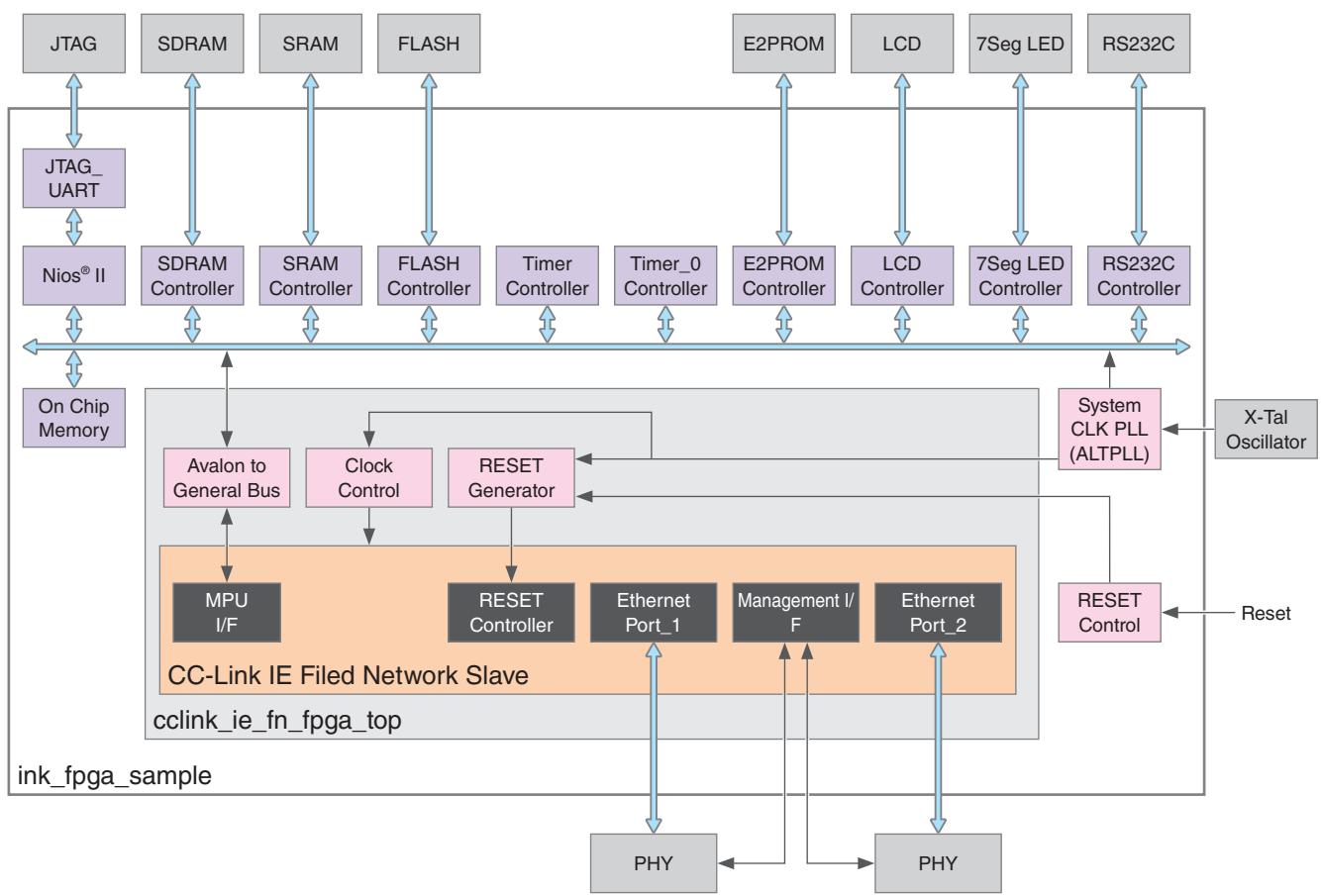


“Monitor tool”

Mitsubishi Electric: GX Works2



Example FPGA Configuration



Deliverables

- Encrypted RTL (Verilog HDL)
- User's manual
- Sample Driver