

MECHATROLINK-III Master/Slave IP

Compatible with Yaskawa Electric JL-100/JL-102 LSI



Overview

MECHATROLINK-III Master/Slave IP together with CPU communicates with the products adapting MECHATROLINK-III standardized by MECHATROLINK Members Association.

Features

- Functionally compatible with JL-100 which is the ASIC for MECHATROLINK-III Master/Slave communication.
- Parameters required for MECHATROLINK-III communication are set either by cpu or through external pins.
- Certified by MECHATROLINK Members Association.

Supported Devices

- Cyclone V
- (* Please contact Macnica sales department about other devices.)

Deliverables

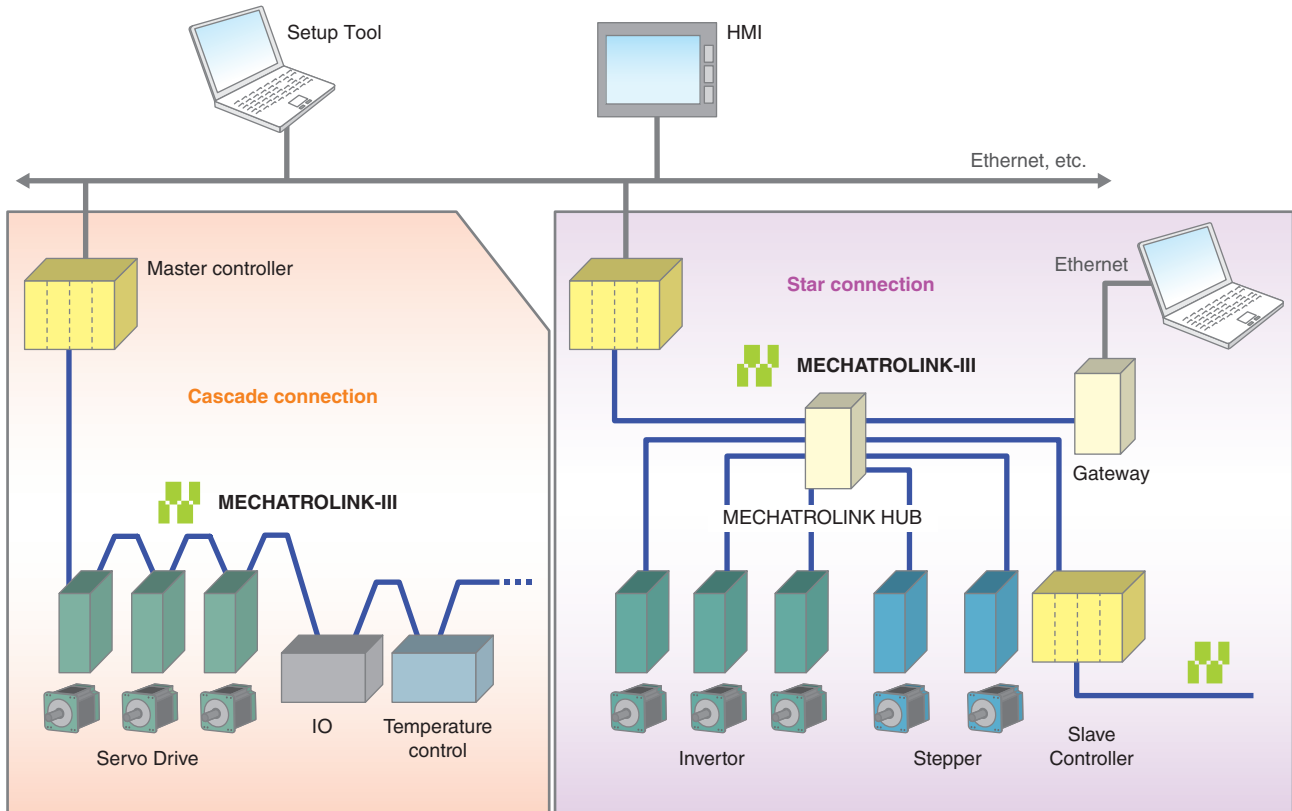
- IP (Encrypted netlist)
- Reference design
- User's manual

Device Resource Utilization

Resource Utilization for 8lane full configuration (including both Transceiver and IP)

Items	Master IP	Slave IP	Multi-Slave IP
ALMs	8,483	8,525	8,468
Total block memory bits	459,776	459,776	459,776
RAM Blocks	62	62	62

System Configuration Example



Evaluation Demo

"Servo Motor"
Yaskawa Electric:
SGMJV-A5ADA21

"ServoPack"
Yaskawa Electric:
SGDV-R70F21A

Macnica:
Sodia - Cyclone V ST SoC Board

System:
SY-M3-03

