MECHATROLINK-III Master/Slave IP
Compatible with Yaskawa Electric JL-100/JL-102 LSI

Overview
MECHATROLINK-III Master/Slave IP together with CPU communicates with the products adapting MECHATROLINK-III standardized by MECHATROLINK Members Association.

Features
• Functionally compatible with JL-100 which is the ASIC for MECHATROLINK-III Master/Slave communication.
• Parameters required for MECHATROLINK-III communication are set either by cpu or through external pins.
• Certified by MECHATROLINK Members Association.

Supported Devices
• Cyclone V
  (* Please contact Macnica sales department about other devices.)

Deliverables
• IP (Encrypted netlist)
• Reference design
• User’s manual

Device Resource Utilization
Resource Utilization for 8lane full configuration (including both Transceiver and IP)

<table>
<thead>
<tr>
<th>Items</th>
<th>Master IP</th>
<th>Slave IP</th>
<th>Multi-Slave IP</th>
</tr>
</thead>
<tbody>
<tr>
<td>ALMs</td>
<td>8,483</td>
<td>8,525</td>
<td>8,468</td>
</tr>
<tr>
<td>Total block memory bits</td>
<td>459,776</td>
<td>459,776</td>
<td>459,776</td>
</tr>
<tr>
<td>RAM Blocks</td>
<td>62</td>
<td>62</td>
<td>62</td>
</tr>
</tbody>
</table>
System Configuration Example

Cascade connection
Star connection

Evaluation Demo

“Servo Motor”
Yaskawa Electric: SGMJV-ASADA21

“ServoPack”
Yaskawa Electric: SGDV-R70F21A

Macnica: Sodia - Cyclone V ST SoC Board

Systec: SY-M3-03